

| Item | Performance | Test Condition |
|--|---|--|
| Electrical Performance | Test | |
| Inductance | Refer to standard electrical characteristics list. | HP4284A,CH11025,CH3302,CH1320,CH1320S LCR Meter. |
| DCR | | CH16502,Agilent33420A Micro-Ohm Mete |
| Operating Temperature | -55°C ~+125°C (Including self - temperature | e rise) |
| Storage temperature and Humidity range Saturation Current | 110~+40℃,50~60%RH (Product with tap 255~+125℃ (on board) | ping) |
| Saturation Current (Isat) | Approximately △L30% | Saturation DC Current (Isat) will cause L0 to drop △L(%) |
| Heat Rated Current (Irms) | Approximately △T40°C | Heat Rated Current (Irms) will cause the coil temperature rise △T(°C). 1.Applied the allowed DC current 2.Temperature measured by digital surface thermometer |
| Reliability Test | | |
| High Temperature Exposure(Storage) AEC-Q200 | | Preconditioning: Run through IR reflow for 2 times. (IPC/JEDEC J-STD-020D Classification Reflow Profiles) Temperature: 125±2°C Duration: 1000hrs Min. Measured at room temperature after placing for 24±2 hrs |
| Temperature Cycling AEC-Q200 | Appearance: No damage. Impedance: within±15% of initial value | Preconditioning: Run through IR reflow for 2 times. (IPC/JEDEC J-STD-020D Classification Reflow Profiles) Condition for 1 cycle Step1: -55±2°C 30min Min. Step2: 125±2°C transition time 1min MAX. Step3: 125±2°C 30min Min. Step4: Low temp. transition time 1min MAX. Number of cycles: 1000 Measured at room temperature after placing for 24±2 hrs |
| Moisture Resistance | Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed the specification value. | Preconditioning: Run through IR reflow for 2 times. (IPC/JEDEC J-STD-020D Classification Reflow Profiles) 1.Baked at50℃ for 25hrs, measured at room temperature after placing for 4 hrs. 2.Raise temperature to 65±2℃ 90-100%RH in 2.5hrs, and keep 3 hours, cool down to 25℃ in 2.5hrs. 3.Raise temperature to 65±2℃ 90-100%RH in 2.5hrs, and keep 3 hours, cool down to 25℃ in 2.5hrs,keep at 25℃ for 2hrs then keep at -10℃ for 3hrs 4.Keep at 25℃ 80-100%RH for 15min and vibrate at the frequency of 10 to 55 Hz to 10 Hz, measure at room temperature after placing for 1~2 hrs. |
| Biased Humidity (AEC-Q200) | | Preconditioning: Run through IR reflow for 2 times. (IPC/JEDEC J-STD-020DClassification Reflow Profiles) Humidity: 85±3% R.H, Temperature: 85°C±2°C Duration: 1000hrs Min with 100% rated current. Measured at room temperature after placing for24±2hrs |



| Item | Performance | | | | Test | Condi | tion | |
|--|--|--|---|--|--|--------------------------------|--|--|
| Reliability Test | | • | | | | | | |
| High Temperature Operational Life (AEC-Q200) | Appearance: No damage. Impedance: within±15% of initial value Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed the specification value. | (IP) Tem Dura | C/J npe atio | erature:12 on :1000h | ΓD-020D 5±2℃ rs Min. w | Classif | ication Re % rated c | eflow Profiles) |
| External Visual | Appearance : No damage | | Inspect device construction, marking and workmanship. Electrical Test not required. | | | | | |
| Physical Dimension | According to the product specification size measurement | Acc | According to the product specification size measurement | | | measurement | | |
| Resistance to Solvents | Appearance : No damage. | Add | d ac | queous was | sh chemic | al - OK | ŒM clear | or equivalent. |
| | Appearance: No damage. Impedance: within±15% of initial value Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed the specification value | Тур | ре | Peak value (g's) | Norm duration (I | | Wave form | Velocity change (Vi)ft/sec |
| Mechanical Shock | | SM | ΙD | 100 | 6 | | Half-sine | 12.3 |
| | | Lea | ad | 100 | 6 | | Half-sine | 12.3 |
| | | shocks in each direction along 3 perpendicular axes. | | | | | | |
| Vibration | | Osc Equ Tota | cilla uipn al A | tion Freque nent : Vibr Amplitude:1 | ency: 10~ ation che .52mm±1 hours (20 | ~2K~1 cker 0%) minut | 0Hz for 2 | low Profiles 0 minute cles each of |
| Resistance to Soldering Heat | Appearance: No damage. Impedance: within±15% of initial value Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed the specification value | Test condition : | | | | | | |
| | | Te | emp | oerature(°C) | Time(s) | ramp | nperature /immersior nersion rat | near cycles i |
| | | | | 260±5 der temp) | 10±1 | 25mm | /s ±6 mm/ | s 1 |
| Thermal shock (AEC-Q200) | | (IP0 Con Step Step Step Nun | C/J nditi p1 p2 p3 nbe | nditioning: FIEDEC J-Sion for 1 cy: -55±2°C 1: 125±2°C 1: 125±2°C 2: red at room | TD-020D cle 5±1min within 200 15±1min : 300 | Classif Sec. | ication Re | eflow Profiles) |

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| Reliability Test | | |
| ESD | Appearance : No damage. | Time (ns) |
| Solderability | More than 95% of the terminal electrode should be covered with solder • | Steam Aging: 16 hours ± 15 min Preheat: 150℃,60sec. Solder: Sn96.5% Ag3% Cu0. 5% Temperature: 245±5℃ ∘ Flux for lead free: Rosin. 9.5% ∘ Dip time: 4±1sec. Depth: completely cover the termination |
| Electrical Characterization | Refer Specification for Approval | Summary to show Min, Max, Mean and Standard deviation |
| Flammability | Electrical Test not required | V-0 or V-1 are acceptable. |
| Board Flex | Appearance : No damage | Preconditioning: Run through IR reflow for 2 times. (IPC/JEDEC J-STD-020D Classification Reflow Profiles) Place the 100mm X 40mm board into a fixture similar to the one shown in below Figure with the component facing down. The apparatus shall consist of mechanical means to apply a force which will bend the board (D) x = 2 mm minimum. The duration of the applied forces shall be 60 (+ 5) sec. The force is to be applied only once to the board. Support Solder Chip Printed circuit board before testing Printed circuit board under test Displacement |



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| Reliability Test | • | • |
| Terminal Strength(SMD) | Appearance : No damage | Preconditioning: Run through IR reflow for 2 times. (IPC/JEDEC J-STD-020D Classification Reflow Profiles) With the component mounted on a PCB with the device to be tested, apply a 17.7 N (1.8 Kg) force to the side of a device being tested. This force shall be applied for 60 +1 seconds. Also the force shall be applied gradually as not to apply a shock to the component being tested. |
| | | |

TIME(sec.) Iron Soldering times: 1 times max.

Fig.2

TIME (sec.)

Reflow times: 3 times max.

Fig1

| Item | Performance | Test Condition | |
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| Reliability Test | _ | | |
| Soldering | · · | ANTEK terminations are suitable for all wave and cannot be avoided, the preferred technique is the | |
| Lead Free Solder re- flow: | Recommended temperature profiles for re-fl | low soldering in Figure 1. | |
| Soldering Iron(Figure 2): | Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended. Note: • Preheat circuit and products to 150℃ • Never contact the ceramic with the iron tip • Use a 20 watt soldering iron with tip diameter of 1.0mm • 355℃ tip temperature (max) • 1.0mm tip diameter (max) • Limit soldering time to 4~5 sec | | |
| Reflow Soldering PRE- tp (245° C / 20- 200 150 60~1 | TP(260°C / 10s max.) | PRE-HEATING SOLDERING NATURAL COOLING within 4~5s 150 Over 60s Gradual cooling | |